

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	cermaic same circuit adj board same (aperture or via) and (copper with bonding or soldering) and stamp and weld\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/14 13:24
L2	0	ceramic same circuit adj board same (aperture or via) and (copper with bonding or soldering) and stamp and weld\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/14 13:24
L3	2	ceramic same circuit adj board same (aperture or via) and stamp and weld\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/14 13:25
L4	3	ceramic same board same (aperture or via) and stamp and weld\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/14 13:39
L5	681	(29/882).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/14 13:39
L6	36	ceramic and I5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/14 13:40
L7	13	ceramic same (hole or via or aperture)and I5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/14 13:48
L8	3	stamp same (hole or via or aperture)and I5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/14 13:49

L9	8704	stamp same (hole or via or aperture)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/14 13:49
L10	309	stamp same (hole or via or aperture)and circuit adj board	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/14 13:54
L11	245	stamp same (hole or via or aperture)and circuit adj board and (connect or connection)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/14 13:56
L12	16	stamp same (hole or via or aperture)and circuit adj board and (connect or connection) and (pierce or deform)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/14 13:58
L13	95	stamp and (hole or via or aperture)and circuit adj board and (connect or connection) and (pierce or deform)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/14 13:59